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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/701,243	11/03/2003	Tal Dayan	76821-200601/US	6103

64494 7590 03/09/2007
GREENBERG TRAUIG, LLP (SV)
IP DOCKETING
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EXAMINER

SHEDRICK, CHARLES TERRELL

ART UNIT	PAPER NUMBER
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2617

SHORTENED STATUTORY PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE
3 MONTHS	03/09/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

Office Action Summary

Application No.

10/701,243

Applicant(s)

DAYAN ET AL.

Examiner

Charles Shedrick

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on 07 December 2006.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-9 and 11-15 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-9 and 11-15 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 24 April 2002 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO/SB/08)
- Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
- Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: _____

DETAILED ACTION

Response to Arguments

Applicant's arguments with respect to claims 1-15 have been considered but are moot in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1, 3-9, 11-15 are rejected under 35 U.S.C. 102(b) as being anticipated by Levesque US Patent Pub. No.: 2002/0065062.

Consider claim 1, Levesque teaches a method comprising: power management system determining a power consumption level of one or more electronic devices coupled to a surface (e.g., abstract, paragraphs 0005-0007); and the power management system controlling a level of power delivered from the surface to the one or more electronic devices coupled to the surface, based on the determined power consumption level of the one or more electronic devices (e.g., paragraphs 0005-0007, 0019-0021, and 0026-0029)

Consider claim 3 and as applied to claim 1, Levesque teaches wherein the power consumption level of the one or more electronic devices is dynamic (e.g., paragraph 0021).

Consider claim 4 and as applied to claim 1, Levesque teaches wherein the determining the power consumption level of the one or more electronic devices further comprises the power

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management system communicating with the one or more electronic devices coupled to the surface (e.g., paragraph 0021).

Consider claim 5 and as applied to claim 1, Levesque teaches wherein the determining further comprises the power management system monitoring activities of the one or more electronic devices coupled to the surface to identify a change in power consumption of the one or more electronic devices (e.g. paragraphs 0005-0007, 0019-0021, and 0026-0029).

Consider claim 6 and as applied to claim 1, Levesque teaches the claimed invention further comprising monitoring radio frequency patterns of the one or more electronic devices to identify the power consumption level of the one or more electronic devices (e.g. paragraphs 0031 and 0032).

Consider claim 7 and as applied to claim 1, Levesque teaches the claimed invention further wherein a power consumption signature is used to determine the power consumption level of the one or more electronic devices (paragraph 0024).

Consider claim 8 and as applied to claim 1, Levesque teaches the claimed invention further comprising the power management system detecting incompatibility with one or more electronic devices coupled to the surface (i.e., a threshold level) (e.g. paragraphs 0022-0024)

Consider claim 9 and as applied to claim 8, Levesque teaches the claimed invention wherein the detecting incompatibility further comprises the power management system communicating its power handling capabilities when the one or more electronic devices coupled to the surface requests a power level that exceeds the power handling capabilities of the management system (e.g. paragraphs 0022-0024).

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Consider claim 11, Levesque teaches a system, comprising: a means for determining a power consumption level of one or more electronic devices coupled to a surface (e.g. paragraphs 0005-0007, 0019-0021, and 0026-0029); and a means for controlling a level of power delivered from the surface to the one or more electronic devices coupled to the surface (e.g. paragraphs 0005-0007, 0019-0021, and 0026-0029), based on the determined power consumption level of the one or more electronic devices (e.g. paragraphs 0005-0007, 0019-0021, and 0026-0029).

Consider claim 12 and as applied to claim 1, Levesque teaches wherein the power management system further comprises power controlling of a serial port on a semiconductor chip (paragraph 0020).

Consider claim 13 and as applied to claim 12, Levesque teaches the claimed invention further comprising the serial port of the semiconductor chip communicating with the one or more electronic devices for recognition of the one or more electronic devices and for power management (paragraph 0020).

Consider claim 14, Levesque teaches a method comprising a power management system determining a power consumption level of one or more electronic devices coupled to a surface (e.g. paragraphs 0005-0007, 0019-0021, and 0026-0029); the power management system controlling a level of power delivered from the surface to the one or more electronic devices coupled to the surface, based on the determined power consumption level of the one or more electronic devices (e.g. paragraphs 0005-0007, 0019-0021, and 0026-0029), and wherein the power management system further comprises a semiconductor chip to facilitate power delivery (paragraph 0020).

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Consider claim 15 and as applied to claim 14, Levesque teaches the claimed invention further comprising the semiconductor chip facilitating communication with the one or more electronic devices to recognize the one or more electronic devices (paragraph 0020).

Conclusion

2. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Charles Shedrick whose telephone number is (571)-272-8621. The examiner can normally be reached on Monday thru Friday 8:00AM-4:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kincaid Lester can be reached on (571)-272-7922. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Charles Shedrick
AU 2617
March 5, 2007



LESTER G. KINCAID
SUPERVISORY PRIMARY EXAMINER